

1 ABSTRACT

2 A decision processor for 802.11b codewords for 1Mb and
3 2Mb data rates includes a sliding correlator for the
4 acquisition of correlation peaks. During a training
5 interval, these correlation peaks are summed into a channel
6 profile memory. The correlation peaks corresponding to a
7 codeword are added into the channel profile memory, and
8 correlation peaks corresponding to the inverse of this
9 codeword are inverted and added into the channel profile
10 memory during the training interval. After the training
11 interval, a decision interval follows whereby correlation
12 peaks are multiplied by the complex conjugate of the
13 contents of the channel profile memory. The multiplication
14 results are accumulated over a codeword window interval to
15 produce a decision output.